



AITA聯盟活動

AI 系統軟體SIG(AISW) 會員會議



主辦者：國立陽明交通大學

會議時間：113年6月28日(星期五) 13:30~17:00

會議地點：陽明交通大學 - 電資大樓蘭成廳203會議室

時 間	議 程	報告人/引言人
13:30-13:40	主辦人開場及主席致詞與說明	陳添福 教授/ 陽明交通大學
13:40-13:50	AISW SIG 近期訊息更新	張永嘉 研發副組長/ 工研院
13:50-14:20	講題一、Memory-efficient Edge AI Inference on the Neural Processing Unit	葉宗泰 教授/ 陽明交通大學
14:20-14:50	講題二、Towards Communication-Aware Dataflow Mapping	張庭榕 教授/ 陽明交通大學
14:50~15:20	Break	
15:20-15:50	講題三、myLLM服務：生成式AI應用之產業優化落地與驗測	陳添福 教授/ 陽明交通大學
15:50-16:55	<ul style="list-style-type: none">成立LLM JDG綜合討論	陳添福 教授/ 陽明交通大學
16:55-17:00	會議總結	



葉宗泰

Tsung-Tai Yeh
助理教授

Tsung Tai Yeh is an assistant professor of computer science at National Yang-Ming Chiao Tung University, Taiwan. He obtained a Ph.D. from the electrical computer engineering school at Purdue University. His research work spans computer architecture, computer systems, and programming languages. He received the Lynn Fellowship at Purdue University and worked at AMD research. His compiler research work was also nominated for the Best Paper Award at the PPOPP conference and was published in multiple top-ranking conference proceedings.



張庭榕

Ting-Jung Chang
助理教授

Ting-Jung Chang is an Assistant Professor in the Department of Computer Science at NYCU. She holds a Ph.D. in ECE and an M.A. in EE from Princeton University. Before joining NYCU, she worked at SambaNova Systems, focusing on exploring interconnects for next-generation AI chips. Her research interests include computer architecture and VLSI design, with a particular emphasis on heterogeneous architectures and emerging technologies.